

**IN THE CLAIMS:**

**On page 10:**

line 1, replace "**PATENT CLAIMS**" with --WHAT IS CLAIMED IS:--;

Please amend claims 1-7 as follows:

*Ent Bl*

1. (Amended) A data [Data] storage device, comprising: [that, in response to a data output request, outputs]  
memory cells having stored data with selectable output addresses;  
wherein said storage device responds to a data output request by  
outputting said stored data beginning with a selected output start address; [,]  
10                   wherein [characterized in that the] selectable output start addresses are spaced [exhibit such slight spacings] from one another such that an [the] amount of data that can be stored between neighboring output start addresses is smaller than an [the] amount of data output in response to said [a] data output request.

2. (Amended) A data [Data] storage device according to claim 1,  
15                   wherein said selected [characterized in that the determination of the] output start address is determined utilizing [to be employed ensues taking] address data applied to said [the] data storage device [into consideration].

3. (Amended) A data [Data] storage device according to claim 2,  
20                   wherein:  
                       said selected [characterized in that the determination of the] output start address is determined by further utilizing [to be employed ensues given additional consideration of] adaptation data applied to said [the] data storage device and; [,]  
                       said [whereby the] adaptation data is related both to said [defined whether and, as warranted, to what extent the] output start address to be  
25                   employed and an [is higher or lower than the] address that is defined by said [the] address data.

4. (Amended) A data [Data] storage device according to claim 3, further comprising:  
output terminals; and  
[characterized in that the adaptation data are employed for controlling] an  
5 interface provided between [the] memory cells of said [the] data storage device  
and said [the] output terminals [of the data storage device];  
wherein said adaptation data are used to control said interface.

5. (Amended) A data [Data] storage device according to claim 4,  
10 wherein said [characterized in that the] interface comprises [contains] a  
multiplexer [(MUX)] that is driven [by the adaptation data or] based on the  
adaptation data [, and with which the data stored beginning with a first output start  
address or the data stored beginning with a second output start address are  
optionally through-connected].

15 6. (Amended) A data [Data] storage device according to claim 8 [5],  
wherein said [characterized in that the] first output start address is an [the] address  
that is represented by said [the] address data applied to said [the] data storage  
device.

7. (Amended) A data [Data] storage device according to claim 8 [5 or  
20 6], wherein said [characterized in that the] second output start address is related  
to, but different from, said [greater or smaller than the] first output start address  
by a scope defined by a [the] wiring of the multiplexer [(MUX)].

Please add the following claims 8-15

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8. A data storage device according to claim 4, wherein data stored with an output start address selected from the group consisting of a first output start address and a second output start address are through-connected.

5 9. A method for outputting data from a data storage device, comprising the steps of:  
receiving a data output request by said data storage device; and  
outputting stored data in a quantity of data that is greater than a quantity of data that can be stored between neighboring output start addresses, and  
beginning said outputting of stored data with a selected output start address which is one of said output start addresses.

10 10. The method according to claim 9, further comprises the steps of:  
applying address data to said data storage device; and  
determining said selected output start address by utilizing said address data.

15 11. The method according to claim 10, further comprising the step of:  
defining adaption data as an indicia related to said address data and said output start address;  
applying said adaption data to said data storage device, wherein said step of determining said selected output start address utilizes said adaption data.

20 12. The method according to claim 11, further comprising the step of:  
controlling, with said adaption data, an interface provided between memory cells of said data storage device and output terminals of said data storage device.

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13. The method according to claim 12, further comprising the steps of:  
controlling a multiplexer contained within said interface by applying said  
adaption data; and  
through-connecting, via said multiplexer, data stored within said data  
storage device beginning with an address selected from the group consisting of a  
first output start address and a second output start address.

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14. The method according to claim 13, further comprising the step of  
calculating said first output start address from said address data applied to said  
data storage device.

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15. The method according to claim 13, further comprising the step of wiring  
said multiplexer so that said second output start address is related to, but different  
from, said first output start address by a scope defined by said wiring.

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**IN THE ABSTRACT**

15      **On page 12:**  
cancel line 2;  
in lines 8-9, cancel “. As a result thereof, the plurality” and substitute --,  
thus minimizing the number-- therefor;  
in lines 9-10, cancel “can be reduced to a minimum”; and  
cancel line 11.

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